

Attorney's Docket No.: 003242.P017

Patent

In re the Application of: Bart Reynolds

(inventor(s))

Application No.: 09/649,437

Filed: August 23, 2000

For: Method and Apparatus for Specifying Addressability and Bus Connections in a Logic Design

(title)

COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, Virginia 22313-1450

SIR: Transmitted herewith is an Amendment for the above-referenced application.

 Applicant claims small entity status. See 37 CFR 1.27.

 X No additional fee is required.

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The fee has been calculated as shown below:

	(Col. 1)		(Col. 2)	(Col. 3)
	Claims Remaining After Amd.		Highest No. Previously Paid For	Present Extra
Total Claims	29	Minus	29	0
Indep. Claims	4	Minus	4	0
<input type="checkbox"/>	First Presentation of Multiple Dependent Claim(s)			

* If the entry in Col. 1 is less than the entry in Col. 2, write "0" in Col. 3.

** If the "Highest No. Previously Paid For" IN THIS SPACE is less than 20, write "20" in this space.

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SMALL ENTITY	
Rate	Additional Fee
X9	\$
X42	\$
+140	\$
Total Add. Fee	\$

OTHER THAN A SMALL ENTITY	
Rate	Additional Fee
X18	\$
X84	\$
+280	\$
Total Add. Fee	\$ 0.00

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Signature

8/22/03

Date

A check in the amount of \$ is attached for presentation of additional claim(s).
 X Applicant(s) hereby Petition(s) for an Extension of Time of 1 month(s) pursuant to
37 C.F.R. § 1.136(a).

 A check for \$ is attached for processing fees under 37 C.F.R. § 1.17.

 Please charge my Deposit Account No. 02-2666 the amount of \$.

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 X The Under Secretary of Commerce for Intellectual Property and Director of the United States
Patent and Trademark Office is hereby authorized to charge payment of the following fees
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 X Any additional filing fees required under 37 C.F.R. § 1.16 for presentation of
extra claims.

 X Any extension or petition fees under 37 C.F.R. § 1.17.

BLAKELY SOKOLOFF TAYLOR & ZAFMAN LLP

Date: August 22, 2003

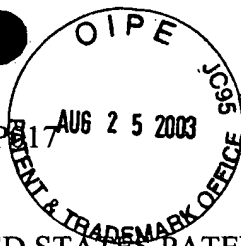
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Mark L. Watson

Reg. No. 46,322

Our Docket No.: 003242.P017



#15/B
10-9-03
[Signature]

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Bart Reynolds

)
) Examiner: Garcia Otero, Eduardo
)

Application No.: 09/649,437

) Art Group: 2123
)

Filed: August 23, 2000

)
)
) For: Method and Apparatus for Specifying
) Addressability and Bus Connections in a
) Logic Design

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AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant respectfully requests the Examiner to enter the following amendments
and to consider the following remarks.

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